

B1
Canceled

a recovery circuit for reconstructing the analog input signal at the [lower reference potential] low voltage level.

Sub
A2

3. (Amended) The current sense integrated ~~circuit~~ of claim 2, wherein the circuit to minimize inherent temperature offset drift comprises a pair of mirrored MOSFETs, such that the circuit has an offset voltage which is equal to the difference between the gate-to-source voltage of the MOSFETs and remains constant over temperature variations.

Please add the following new claim:

Sub
B3
A3

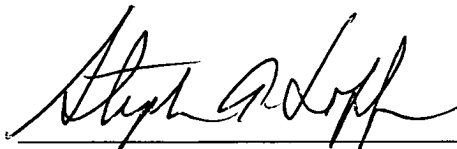
5. The current sense integrated circuit of claim 1, wherein the level shift circuit comprises a pulse generator circuit for producing rising edge triggered pulses and falling edge triggered pulses from the pulse width modulated signal and a pair of MOSFETs for receiving the rising edge triggered pulses and the falling edge triggered pulses and transposing those pulses from a high voltage level to a low voltage level.

REMARKS

Claim 1 has been amended. Claim 4 has been canceled. Claim 5 has been added.

Prompt examination and issuance is respectfully requested.

Respectfully submitted,



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